

5.5V Full-Function 2.5A DC Load Switch with Adjustable Current

Limit

Features

- Input voltage: 1.8V to 5.5V
- Quiescent supply current 80µA with open load
- Turn-on controlled
- Continuous Current Max: 2.5A
- 0.045A-2.5A Current
- P-channel MOSFET current-limited architecture
- Under-Voltage Lockout
- Over-Voltage Lockout
- Low shutdown current
- Fast current limit response
- Thermal shutdown protection
- Reverse current blocking
- Package: six ball advanced 0.98x1.48mm
 WLCSP-6

Applications

- Handheld electronic device
- Portable Enterprise / Industrial Devices
- Digital Cameras
- Peripheral Ports and Accessories
- Medical Equipment
- Hot Swap

Descriptions

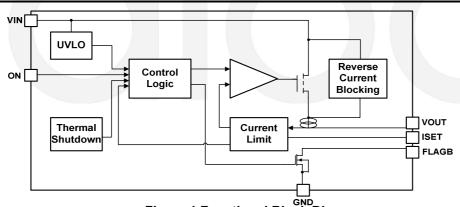
The DIO7195B is load switch that provides full protection to systems and loads that may encounter large current conditions.

A P-channel MOSFET current-limit makes the current prevented, when the MOSFET is off. And the output voltage higher than input in addition. The perfectly thermal shutdown protection that shuts off the switch to prevent damage to the part when a continuous over-current condition causes excessive heating.

When the switch current reaches the current limit, the parts operate in a constant-current mode to prohibit excessive currents from causing damage.

The DIO7195B does not turn off after a current limit fault, but remains in the constant-current mode indefinitely.

Block Diagram





Ordering Information

Order Part Number	Top Marking		TA	Package	
DIO7195BWL6	D795	Green	-40 to 85°C	WLCSP-6	Tape & Reel,3000

Pin Assignment

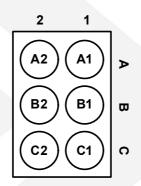


Figure 2 Pin Assignment (bottom view)

Pin Definitions

Pin Name	Name	Pin Description			
		Fault Output. Active low, open-drain output that indicates an over-current supply, undervoltage, or over-temperature state.			
B1	VOUT	Switch Output. Output of the power switch.			
C1	ISET	Current Limit Set Input. A resistor from ISET to ground sets the current limit for the switch.			
C2	GND	Ground.			
B2	VIN	Supply Input. Input to the power switch and the supply voltage.			
A2	ON	Active high, ON control input.			



Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Rating" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameter			Rating	Unit	
All pins			-0.3 to 6	V	
Package Thermal Resistance θ _{JA}			85	°C/W	
Junction Temperature Range			150	°C	
Lead Temperature (Soldering, 10 sec.) Storage Temperature Range (T _{STG}) ESD Susceptibility HBM (Huma			260	°C	
			-65 to 150	°C	
		nan Body Mode)	7	kV	

Note: Input and output negative ratings may be exceeded if input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation to ensure optimal performance to the datasheet specifications. DIOO does not recommend exceeding them or designing to Absolute Maximum Ratings.

Parameter	Rating	Unit	
VIN	1.8 to 5.5	V	
All other pins	0 to 5.5	V	
Junction Temperature Range	-40 to 125	°C	
Ambient Temperature Range	-40 to 85	°C	



Electrical Characteristics

T_A=25°C V_{IN} =3.3V, unless otherwise noted.

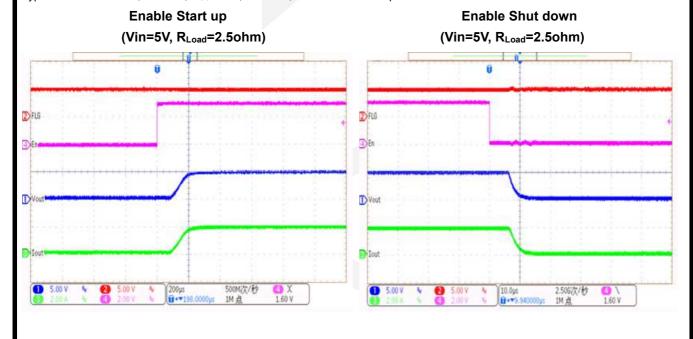
Symbol	Parameter	Conditions		Min.	Тур.	Max.	Uni
V _{IN} Input Voltage Range				1.8		5.5	V
			V _{IN} =1.8V		70		μA
I_Q	Quiescent Supply Current	I _{OUT} =0mA, V _{ON} =V _{IN}	V _{IN} =3.3V		75		μA
		VOIN VIIN	V _{IN} =5.5V		80		μA
-	0.0	T _A =25°C,I _{OUT} =20	00mA		55	80	mΩ
R _{DS(ON)}	On Resistance	T _A =-40 to 85°C,	I _{OUT} =200mA			135	
	On Input Logic High	V _{IN} =1.8V		0.8			
V _{IH}	Voltage On	V _{IN} =5.5V		1.4			V
	On Input Logic Low	V _{IN} =1.8V				0.5	V
V_{IL}	Voltage	V _{IN} =5.5V				1.0	
I _{IN}	On Input Leakage	V _{ON} =V _{IN} or GND		-1	0	1	μA
V _{IN_SD}	V _{IN} Shutdown Current	V _{ON} =0V, V _{IN} =5.5V, V _{OUT} =Short to GND		-2		2	μA
	FLAGB Output Logic Low	V _{IN} =5V, I _{SINK} =2mA			0.05	0.20	V
V_{FLB_L}	Voltage	V _{IN} =1.8V, I _{SINK} =2mA			0.12	0.30	V
I _{FLB_H}	FLAGB Output Logic High Leakage Current	V _{IN} =5V, Switch ON				1	μΑ
I _{SDT}	VOUT Shutdown Current	V _{ON} =0V,V _{OUT} =5.	5V, V _{IN} =Short-to-GND	-2		2	μΑ
V _{BREAKDOWN}	Reverse Breakdown Voltage	V _{IN} =V _{ON} =0V, I _{OUT} =200μA			8		١
/	Current Limit		R _{SET} =430Ω		2.5		- A - mA
		V _{IN} =4.5V	R _{SET} =680Ω		1.6		
I _{LIM}			R _{SET} =910Ω	0.9	1.0	1.1	
			R _{SET} =1.5KΩ		0.5 45		
	T. 101.11	R_{SET} =15.8KΩ Shutdown Threshold			140		°C
-	Thermal Shutdown Temperature	Return from Shu			130		°C
T_{SD}	Thermal Shutdown Hysteresis				10		°C
V _{OVLO} Over-Voltage Lockout		V _{IN} Increasing		5.6	5.8	6.1	V



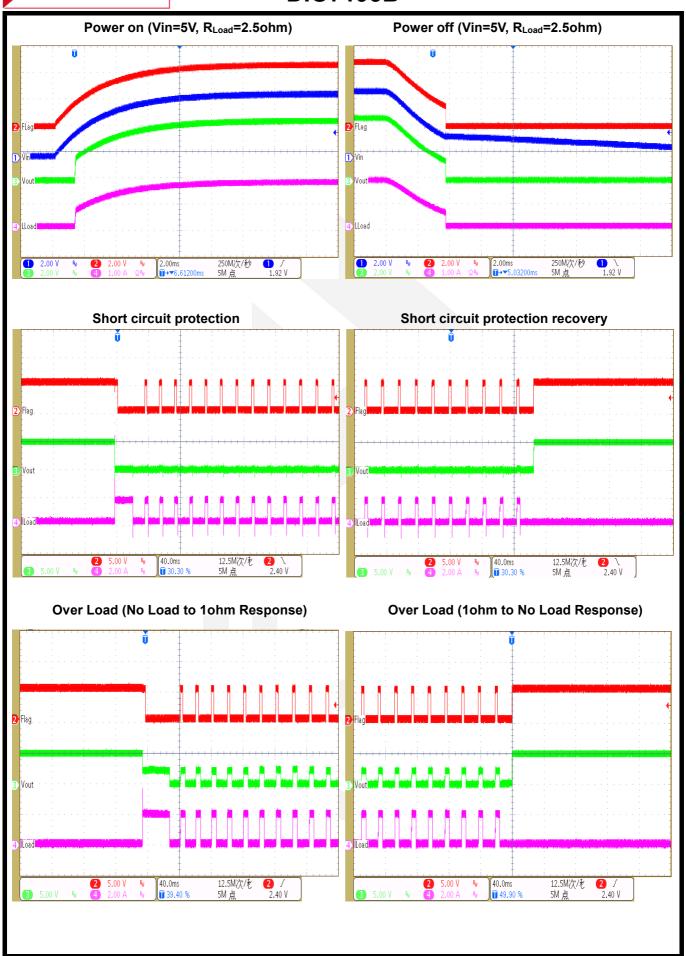
	V _{OVLO_HYST}	Over-Voltage Lockout Hysteresis			200		mV	
	TF_D	Delay time	From current limit to FLAGB		3		ms	
V	V _{UVLO}	Under-Voltage Lockout	V _{IN} Increasing	1.55	1.65	1.75	V	
	V _{UVLO_HYST}	Under-Voltage Lockout Hysteresis			50		mV	
	td _{ON}	Delay On Time	R _L =500Ω, C _L =0.1μF		80		μs	
	t _R	V _{OUT} Rise Time	R _L =500Ω, C _L =0.1μF		80		μs]
	t _{ON}	Turn On Time	R _L =500Ω, C _L =0.1μF		160		μs	
•	td _{OFF}	Delay Off Time	R _L =500Ω, C _L =0.1μF		7		μs	
•	t _F	V _{OUT} Fall Time	R _L =500Ω, C _L =0.1μF		140		μs	
•	t _{OFF}	Turn Off Time	R _L =500Ω, C _L =0.1μF		146		μs	
	t _{sc}	Short-Circuit Response Time	V _{IN} =V _{OUT} =3.3V		5		μs	

Typical Performance Characteristics

Typical value: V_{IN}=3.3V, C_{IN}=1uF, C_{OUT}=1uF, T_A=25°C, unless otherwise specified.









Application Information

The DIO7195B is current-limited switch that protects system and load from damaged or disrupted by the application of high currents. The controller protects against system malfunctions through current limiting, undervoltage lockout, and thermal shutdown.

Current Limit

The current limit ensures the current through the switch does not exceed the maximum. The DIO7195B does not including limit blanking period, so it remains in a constant current state until the ON pin is deactivated or the thermal shutdown turns off the switch. The current limit is set with an external resistor connected between ISET and GND. This resistor is selected using the following equation:

$$I \lim \approx \frac{910}{RSET}(A)$$

A short-circuit detection feature is introduced to preventing the switch from large power dissipation during heavy load. The switch is put into short- circuit current-limiting mode if the switch is loaded with a heavy load.

The DIO7195B has no current limit blanking period. When the output voltage drops below 1.1V, the short-circuit detection threshold voltage, the current limit value is re-conditioned and the short-circuit current-limit value is decreased, this value is about 60% of typical current limit. This keeps the power dissipation of the part below a certain limit even at dead-short conditions at 5.5V input voltage.

On/Off Control

The ON/Off pin controls the state of the switch. The DIO7195B does not turn off in response to an over-current condition, but remains operating inconstant-current mode as long as ON is active and the thermal shutdown or under-voltage lockout have not activated.

Fault Report

FLAGB signals the fault mode by activating low up on the detection of an over-current, input under-voltage, over-voltage or over-temperature condition, the FLAGB goes low at the end of the blanking time, and remains low during the faults and immediately returns high at the end of the fault condition. FLAGB is an open-drain MOSFET that requires a pull-up resistor between VIN and FLAGB. During shutdown, the pull-down on FLAGB is disabled to reduce current draw from the supply.

Under-Voltage Lockout (UVLO)

The under-voltage lockout turns the switch off if the input voltage drops below the under-voltage lockout threshold. With the ON pin active, the input voltage rising above the under-voltage lockout threshold causes a controlled turn-on of the switch, which limits current over shoot.

Thermal Shutdown

The thermal shutdown protects the die from internally or externally generated excessive temperatures. During an over-temperature condition, FLAGB is activated and the switch is turned off. The switch automatically turns on again if temperature of the die drops below the threshold temperature.



Reverse-Current Blocking

The reverse-current blocking feature that protects the input source against current flow from output to input. For a standard USB power design, this is an important feature to protect the USB host from being damaged due to reverse current flow on V_{BUS}.

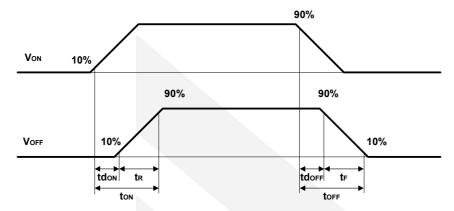


Figure 3 Timing diagram

td_{ON}=Delay On Time; t_R= V_{OUT} Rise Time; t_{ON}=Turn-On Time.

 td_{OFF} = Delay Off Time; t_F = V_{OUT} Fall Time.

toff=Turn-Off Time.

Typical Application

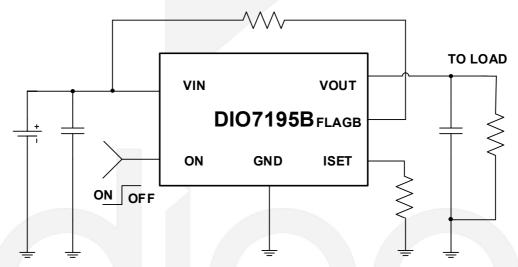


Figure 4 Typical application



CONTACT US

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